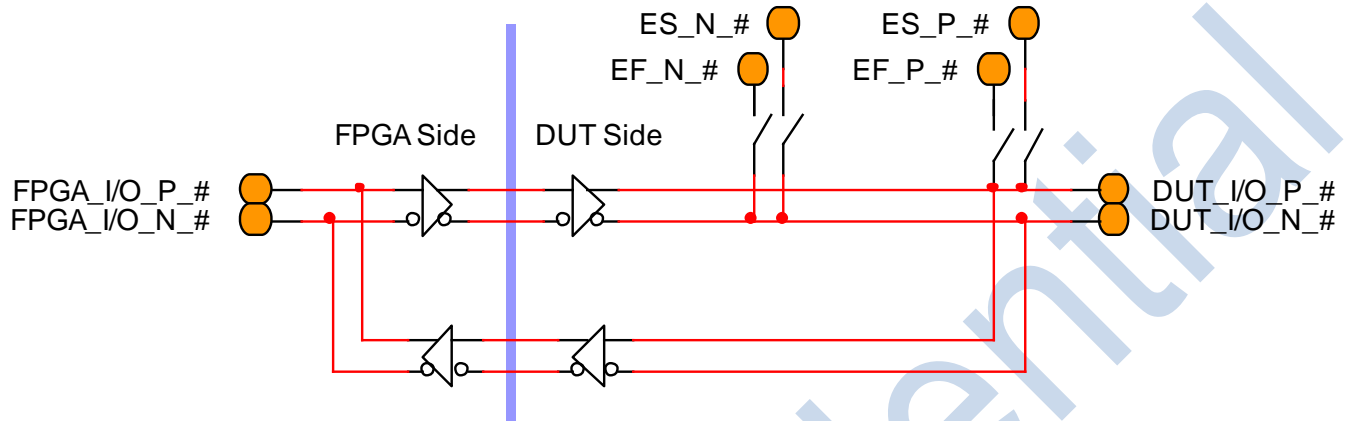


ANALOG TEST ENGINES



PE15 – Quad Channel

10 Gb/s Differential / Single-Ended Pin Electronics

DUT Interface / FPGA Interface

High-Speed Pin Electronics – Driver

- 10 Gb/s Fmax
- Programmable High and Low Levels
- Programmable Driver Pre-Compensation
- 50Ω Output Impedance
- ≤600 mV swing over a 1.2V Range
- Supports HiZ
- Supports Single or Double Termination

High-Speed Pin Electronics – Comparator

- 10 Gb/s Fmax
- Differential Receiver
- Internal Input Terminations / Pull-Ups

Fanout Capability

- 1:2 FPGA-to-DUT Fanout
- 1:2 DUT-to-FPGA Fanout

Signal Routing 2X2 Cross Point Switch

- 2:2 FPGA Signal XPS on Channel Pairs
- 2:2 DUT Signal XPS on Channel Pairs

PMU

Integrated Switches for an External PMU

- External Force Switch per Wire
- External Sense Switch per Wire

Low-Power PE Mode

- Low-Power Drive +3.0V / -1V
- Low-Power Receive +3.0V / -1V

2 Pin Serial Port

- 0.9V to 1.8V Compliant
- Selectable Integrated Terminations

Power / Package

- 10 mm X 10 mm QFN, Lead Free
- 72 Lead, 0.5 mm Lead Pitch
- Exposed Heat Slug on Bottom
- 125 to 250 mW / Channel

Recommended Applications

- SERDES Testing
- High-Speed Memory Testing
- Differential Channel Testing