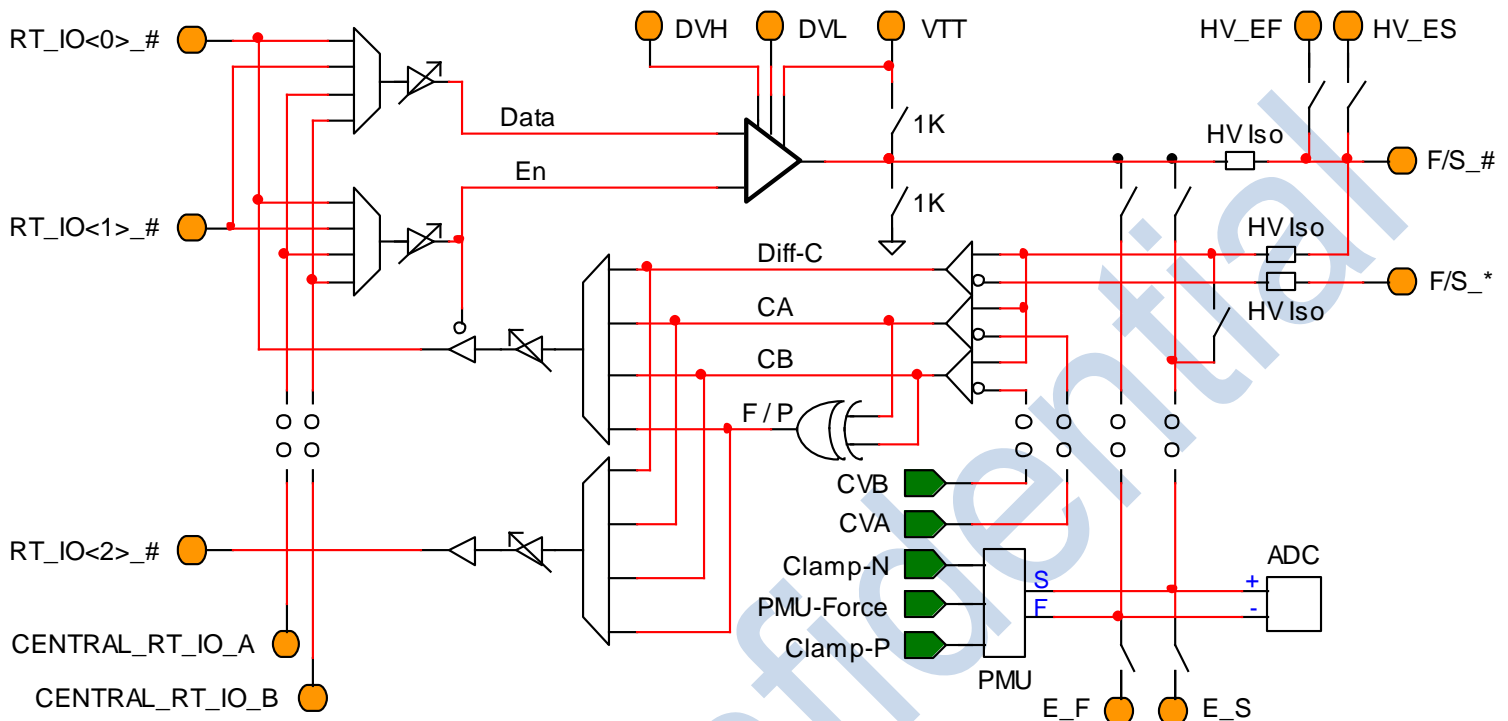


# ANALOG TEST ENGINES



## PE77 – 8 Channel, 5V, 500 Mb/s Pin Electronics w/ 15V SV

### Pin Electronics – Driver

- 500 Mb/s, 3 Level Driver, Differential Driver
- -1.0V to +4.5V Output Range
- 1K $\Omega$  HiZ Pull-Down to Ground Option
- 1K $\Omega$  HiZ Pull-Up to VTT Option

### Pin Electronics – Comparator

- 500 Mb/s Fmax
- -2.0V to +5.0V Input Range
- Dual comparators w/ on-chip thresholds
- Differential comparator per channel-pair
- Pass / Fail Mode

### Deskew

- Coarse Delay (5 ns range, 40 ps res)
- Pulse Width Adjust ( $\pm 256$  ps / 4 ps Res)

### FPGA Real Time Interface

- Flexible FPGA Interface
- 2 or 3 FPGA Pins / Channel
- 2 Central FPGA Pins / Chip
- Asynchronous I/O
- CMOS I/O w/ On-Chip Terminations
- Technology Independent (Adjustable) I/O

### Super Voltage / High Voltage EF / ES

- +15V Super Voltage Under Pattern Control
- 10 mA Imax

### PMU

- -1.0 to +4.0V Measure Range
- -0.75 to +4.0V Force Range
- 5 Current Ranges (4  $\mu$ A to 40 mA)
- FV / FI / MV / MI
- Voltage / Current Clamps
- F/S\_# Continuity Checker
- DUT Ground Continuity Checker

### Measurement System

- Integrated 16-Bit ADC
- Integrated Force & Sense Switches

### Built-In Self Protection

- Over Temperature Alarm

### Package / Power

- 10 mm X 10 mm QFN, Lead Free
- Exposed heat slug on bottom
- 72 Lead, 0.5 mm lead pitch
- 75 to 190 mW / Channel (Pd<sub>q</sub>)