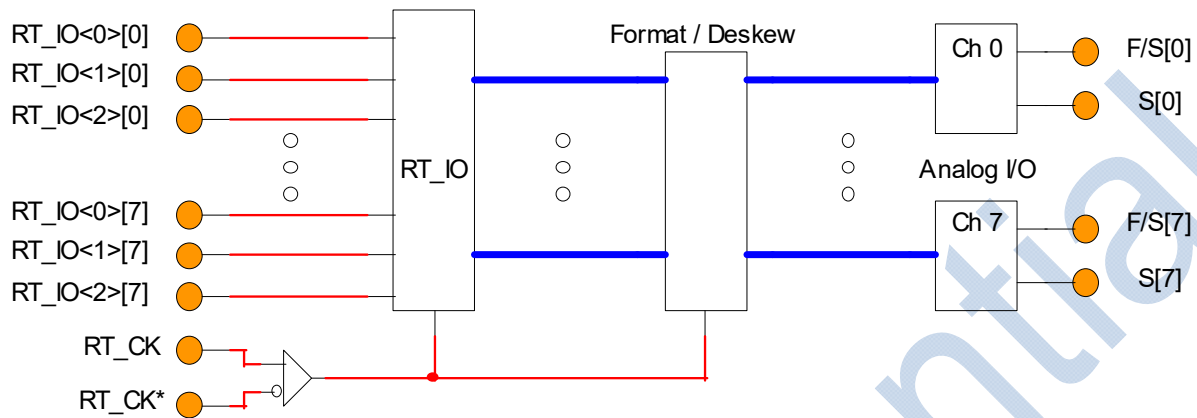


# ANALOG TEST ENGINES



## UTP100 – Octal, 200 Mb/s, 6V, 400 mA Universal Test Pin

### Pin Electronics – Driver

- 50Ω / 5Ω Options
- 200 Mb/s Fmax
- Programmable Precompensation
- 3 Level Driver w/ on-chip levels
- -1.0V to +5.0V Output Range

### Measurement Capability

- MV / MI Capability
- 2 Bit “Go / No Go” Capability
- Per channel MV / MI SAR ADC
- 2 Independent Analog Monitors (for use with an External ADC)

### Pin Electronics – Comparator

- 200 Mb/s Fmax
- Dual comparator w/ on-chip thresholds
- -1V to +5V Input Range

### Built In Self Protection

- Over Temperature Protection / Alarm
- Over Current Protection / Alarm
- Current Clamp Protection / Alarm

### Real Time Data I/O

- Synchronous / Asynchronous Interface
- Reduced FPGA pin Count Options

### 2 Pin Serial Port

### Power / Package

- 72 Lead, 10 mm X 10 mm QFN
  - Lead Free
  - Exposed Heat Slug on the Bottom
  - Extremely low ΘJC
- 100 mW ≤ Pdq / Channel ≤ 250 mW
- Separate DPS Positive Power Supply

### Deskew

- Coarse Delay (625 ps resolution)
- Fine Delay (50 ps resolution)
- per channel, per function (drive, compare)

### Recommended Applications

- Burn-In Test
- Low Cost Functional Test
- BIST / Scan Test
- In-Circuit PCB Test

### DPS / PMU / V/I

- FV / FI / MV / MI Capability
- DPS: 0V to +4V Output Range
- PMU: -1V to +5V Output Range
- 6 Current Ranges (409.6 mA to 4.096 μA)
- Programmable Voltage / Current Clamps
- Ganging Capability
- Power Amplifier Controller Mode
  - High Voltage Operation
  - High Current Operation